

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of )  
Teruaki KANZAKI ) Group Art Unit: 2183 (expected)  
Application No.: Unassigned ) Examiner: W. M. Treat (expected)  
Filed: August 13, 2003 ) Confirmation No.: Unassigned  
For: TRACE CONTROL CIRCUIT )  
ADAPTED FOR HIGH-SPEED )  
MICROCOMPUTER OPERATION )

**FIRST  
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant hereby submits the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98.

Pursuant to 37 C.F.R. § 1.98, a copy of each of the non-U.S. Patents documents cited is enclosed.

U.S. Patent No. 6,463,551 to Kanzaki et al., issued October 8, 2002;  
U.S. Patent No. 6,154,857 to Daniel Peter Mann, issued November 28, 2000;  
U.S. Patent No. 5,933,626 to Mahalingaiah et al., issued August 3, 1999;  
U.S. Patent No. 5,625,785 to Miura et al., issued April 29, 1997;  
U.S. Patent No. 5,574,937 to Bikas Narain, issued November 12, 1996;  
U.S. Patent No. 6,523,136 to Motoki Higashida, issued February 18, 2003;  
Japanese Patent Publication No. 5-197549, issued August 6, 1993;  
Japanese Patent Publication No. 3-266140, issued November 27, 1991;  
Japanese Patent Publication No. 3-240839, issued October 28, 1991; and  
"Logic Interface to Allow High Performance Event Tracing With Low Performance Monitoring Devices", IBM Technical Disclosure Bulletin, vol. 29, no. 5, p. 2076-2079, October 1986.

Information Disclosure Statement  
Application No. Unassigned  
Attorney's Docket No. 027260-670  
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The documents are being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since documents are being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

The Examiner's attention is directed to copending application number 09/588,604, naming Teruaki Kanzaki as inventor(s) and filed on June 7, 2000, and the documents cited therein, a copy of which is enclosed.

**[Redacted]** In accordance with M.P.E.P. § 609(c)(2) (August 2001, page 600-131), the Office is requested to return a copy of this Information Disclosure Statement with the Examiner's initials adjacent to this paragraph indicating that this copending application has been considered. By citation to the copending application, confidentiality is not waived and the Office is requested to maintain the confidentiality of the copending application under 35 U.S.C. § 122.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By:

Ellen Marcie Emas  
Registration No. 32,131

Date: August 13, 2003

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Substitute for forms 1449A/PTO & 1449B/PTO	ATTORNEY'S DKT No. 027260-670	APPLICATION NO. Unassigned
<b>FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	APPLICANT Teruaki KANZAKI	
	FILING DATE August 13, 2003	GROUP 2183 (expected)

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

## **NON PATENT LITERATURE DOCUMENTS**

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	"Logic Interface to Allow High Performance Event Tracing With Low Performance Monitoring Devices", IBM Technical Disclosure Bulletin, vol. 29, no. 5, p. 2076-2079, October 1986

**Examiner Signature** \_\_\_\_\_ **Date Considered** \_\_\_\_\_

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **SEND TO:** Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.